

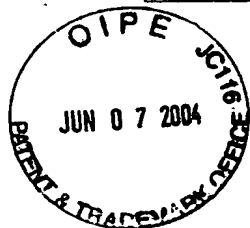
JFW

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Dated: June 2, 2004

Signature: \_\_\_\_\_

(Raymond Garguilo, Jr.)



Docket No.: TESSERA 3.0-241 DIV  
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
Owais Jamil

Application No.: 10/728,306

Filed: December 4, 2003

For: METHOD OF MAKING A  
MICROELECTRONIC PACKAGE  
INCLUDING A COMPONENT HAVING  
CONDUCTIVE ELEMENTS ON A TOP  
SIDE AND A BOTTOM SIDE THEREOF

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:  
: Group Art Unit: 3729  
:  
: Examiner: Donghai D.  
Nguyen

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE**

Dear Sir:

In response to the Official Action mailed May 3, 2004, applicant hereby elects Species A, namely claims 3-7 as well as claims 1 and 2, for examination in the present application. Applicant submits the following remarks.